

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Kazuo AISAKA et al.

Appln. No.:

Filed: HERewith

For: CACHE MEMORY ALLOCATION METHOD

* * *

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified
patent application as indicated below.

IN THE CLAIMS:

Claim 8:

line 1, delete "or 7".

Claim 9:

line 1, delete "or 7".

Please add the following claims:

- 1 --22. The operating system according to claim 7,
- 2 wherein:
- 3 the allocating resources includes determining the
- 4 number of pages of the cache memory to be assigned for the

09688360-101200